

ABSTRACT OF THE DISCLOSURE

A path transistor circuit comprises a plurality of path transistors connected in parallel. Same input signal is inputted into the sources of these path transistors.

5 Continuities of these path transistors is controlled by a plurality of control signals having an exclusive relationship therebetween. A plurality of buffers respectively drive the drive segments including at least the path transistors and wirings. The drive segments being a plurality of divided

10 ranges each having an equal potential.

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